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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/491,900	01/27/2000	M. Jason Welch	10991989-1	9214
22878	7590 03/22/2002			
AGILENT TECHNOLOGIES, INC. INTELLECTUAL PROPERTY ADMINISTRATION, LEGAL DEPT. P.O. BOX 7599 M/S DL429			EXAMINER	
			NGUYEN, DILINH P	
	LOVELAND, CO 80537-0599		ART UNIT	PAPER NUMBER
,			2814	<u> </u>
		DATE MAILED: 03/22/2002		

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)		
Advisory Action	09/491,900	WELCH ET AL.		
Advisory Action	Examiner	Art Unit		
	DiLinh Nguyen	2814		
The MAILING DATE of this communication appe	ars on the cover sheet with the c	orrespondence address		
THE REPLY FILED 18 March 2002 FAILS TO PLACE TI Therefore, further action by the applicant is required to av final rejection under 37 CFR 1.113 may <u>only</u> be either: (1) condition for allowance; (2) a timely filed Notice of Appeal Examination (RCE) in compliance with 37 CFR 1.114.	oid abandonment of this applicated at the control of the control o	ation. A proper reply to a		
PERIOD FOR RE	PLY [check either a) or b)]			
a) The period for reply expires 3 months from the mailing date b) The period for reply expires on: (1) the mailing date of this A no event, however, will the statutory period for reply expire to ONLY CHECK THIS BOX WHEN THE FIRST REPLY WAS 706.07(f). Extensions of time may be obtained under 37 CFR 1.136(a). The fee have been filed is the date for purposes of determining the period of fee under 37 CFR 1.17(a) is calculated from: (1) the expiration date of the (2) as set forth in (b) above, if checked. Any reply received by the Office	Advisory Action, or (2) the date set forth ater than SIX MONTHS from the mailing FILED WITHIN TWO MONTHS OF The date on which the petition under 37 CF of extension and the corresponding amount the shortened statutory period for reply	g date of the final rejection. HE FINAL REJECTION. See MPEP R 1.136(a) and the appropriate extension from the fee. The appropriate extension from the fee or the final Office action; or		
timely filed, may reduce any earned patent term adjustment. See 37 C	FR 1.704(b).			
1. A Notice of Appeal was filed on Appellant's 37 CFR 1.192(a), or any extension thereof (37 CFF	Brief must be filed within the per R 1.191(d)), to avoid dismissal o	eriod set forth in f the appeal.		
2. \boxtimes The proposed amendment(s) will not be entered be	ecause:			
(a) X they raise new issues that would require further	er consideration and/or search (see NOTE below);		
(b) they raise the issue of new matter (see Note b	elow);			
(c) they are not deemed to place the application in issues for appeal; and/or	n better form for appeal by mate	rially reducing or simplifying the		
(d) they present additional claims without canceli	ng a corresponding number of f	inally rejected claims.		
NOTE: See Continuation Sheet.				
3. Applicant's reply has overcome the following rejecti	on(s):			
4. Newly proposed or amended claim(s) would canceling the non-allowable claim(s).	be allowable if submitted in a se	eparate, timely filed amendment		
5. The a) affidavit, b) exhibit, or c) request for application in condition for allowance because:		idered but does NOT place the		
6. The affidavit or exhibit will NOT be considered becaraised by the Examiner in the final rejection.	ause it is not directed SOLELY	to issues which were newly		
7. For purposes of Appeal, the proposed amendment explanation of how the new or amended claims we	(s) a) will not be entered or bould be rejected is provided belo)∏ will be entered and an ow or appended.		
The status of the claim(s) is (or will be) as follows:				
Claim(s) allowed:		b		
Claim(s) objected to:				
Claim(s) rejected: 1-10, 17-18 and 20.				
Claim(s) withdrawn from consideration:				
8. The proposed drawing correction filed on is	a) approved or b) disapp	proved by the Examiner.		
9. Note the attached Information Disclosure Statement	nt(s)(PTO-1449) Paper Ho(s)			
10. Other: Doubles WillE				
	Dov61-	13 WILLE		

Continuation of 2. NOTE: Newly added limitation, such as: a first port located in a first area of integrated circuit real estate, for outputting a signal, the first port extends directly into the common area from a first area; and a plurality of wire-tracing levels, would require further consideration and /or search.